Supermode Control in Integrated Hybrid Si/III–V Optoelectronic Circuits for Modal Gain Enhancement

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Abstract: We propose using supermode control to enhance the modal gain in integrated hybrid Si/III–V optoelectronic circuits. Numerical simulations predict that a 4-fold enhancement in modal gain can be achieved with optimal design.

Keywords: Optoelectronic integrated circuits; Integrated optics; Si photonics; Semiconductor devices; Modal control.

1. INTRODUCTION

The realization of light generation and amplification in silicon (Si) remains an elusive goal in optical communication research. As well known, Si is an indirect bandgap material thus a very poor convertor of electricity to light. Moreover, due to the crystal centrosymmetry, 2nd-order nonlinearities are also highly suppressed in bulk Si. Recent efforts in obtaining optical gain in/on Si include employing Raman amplification [1], doping rare-earth ions [2], engineering nano-crystalline Si structures [3], and monolithic growth of Ga(NAsP) via GaP buffer layer [4].

So far the most promising approach is based on a “hybrid evanescent platform” in which an active III–V (AlGaInAs) slab is bonded on top of a pre-patterned Si-on-insulator (SOI) wafer by a low-temperature wafer bonding technique [5, 6]. Under operation, the bonded structure supports a joint optical mode: the mode is designed to be mostly confined to Si waveguide and amplified by the penetration of the evanescent tail into the III–V slab. The tradeoff between the confinement factors (Γ) in Si waveguide and in III–V slab is the major handicap of this scheme: A high coupling efficiency between the hybrid laser and the rest of the SOI photonic circuit requires that ΓSi be close to 1, thus reducing ΓQW and the modal gain. Because of the small modal gain, the demonstrated lasers have low emission efficiency compared to traditional III–V lasers.

In this paper we propose controlling the optical mode in such hybrid Si/III–V structures, which eliminates, in principle, the basic compromise inherent in the evanescent laser design since the full modal power, rather than the evanescent tail, is available for amplification. This results in a larger modal gain and increased efficiency.

2. SUPERMODE CONTROL IN SI/III–V HYBRID LASERS

Our proposed laser utilizes a system of two proximity-coupled waveguides (waveguides 1 and 2, as shown in Fig. 1). The first guide is made of an III–V amplifying material while the second is a Si guide fabricated in the SOI substrate. The “supermode” refers to the local eigenmode of this coupled-waveguide system along its propagational direction, which can be expressed as column vectors with their components being the amplitudes of the two individual waveguide modes [7]

where \( \delta = (\beta_2 - \beta_1)/2 \) is the mismatch of propagation constants between the individual uncoupled waveguide modes, \( 2S = \)
2(δ̅2+κ̅)1/2 = β̅e−β̅o is the difference of propagation constants of the supermodes, and κ is the coupling strength between the two waveguides. Note that the modal profiles E̅ and E̅o are space dependent via the parameters δ and S. For both supermodes, when δ < 0 and |δ| ≫ κ, the mode substantially resides in one waveguide, and when δ > 0 and δ ≫ κ, the mode is substantially in the other waveguide. At the phase-matching point where δ = 0 the mode is evenly distributed in the two waveguides [7].

Therefore, as shown in Fig. 2, it is possible, by a proper choice of the width of the Si waveguide, to direct the modal power to the amplifying III–V waveguide or to the Si waveguide, thereby avoiding the degraded performance which results from the reliance on the evanescent field. The modal power is then diverted to the Si waveguide by adiabatically increasing its width. This makes for efficient coupling to the outside Si photonic circuit.

As marked with the red line in Fig. 2, the mode starts propagating from left to right in the upper left III–V waveguide where it is amplified. It then enters the adiabatic transformer section where the width of the Si waveguide increases so as to cause δ to change from δ < 0 to δ > 0. This causes the modal energy to shift to the lower low-loss Si waveguide where it is partially reflected from the right output facet. The reflected field retraces its path till reflected from the upper left facet thus completing the round trip.

A considerable enhancement of modal gain is the most direct advantage of the supermode control. Assuming the identical layer structure used in [5], we have simulated the confinement factors, for the even mode, in the active region (including quantum wells and barriers in between), Γact and in the Si guide, ΓSi, for different Si guide widths. By evanescent coupling there exists a tradeoff between Γact and ΓSi. A fixed Si guide width of 1.10 μm gives Γact = 0.067 and ΓSi = 0.757. By our proposed supermode control, if we vary the Si guide width near the output facet from at main body 0.75 μm to at the output facet 1.35 μm, we will have at main body Γact = 0.268, and at the output facet ΓSi = 0.892. Therefore Γact hence the modal gain, increases from 0.067 to 0.268, i.e., a factor of 4. In the meantime the output coupling efficiency is also enhanced by, a less impressive, 18%.

3. PERSPECTIVE OF NEXT-GENERATION “SUPERMODE SI/III–V HYBRID CIRCUITY”

The main advantage of this “spatial switching” of the modal power is to obtain maximum achievable gain in the inverted medium since the peak modal field and not just an evanescent tail induce the amplifying transitions in the gain (III–V) region. The same principle of spatial switching of the modal power can be used to achieve other functions. For example: To make an optical amplifier, we can switch the input modal power from a wide Si waveguide (δ > 0) to a III–V amplifying section (δ < 0) and back again to the wide Si waveguide (δ > 0). This principle can also be applied to make a modulator or a photodetector.

It is envisioned that once the individual optical functional devices are achieved in a much more efficient fashion by using the supermode control, the next-generation, large-scale, high-efficiency Si/III–V hybrid circuitry will not be far away (see Fig. 3), and we expect a thorough revolution in the development of optical communications.

REFERENCES