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Fast on-wafer electrical, mechanical, and electromechanical characterization of piezoresistive cantilever force sensors

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Validation of a technological process requires an intensive characterization of the performance of the resulting devices, circuits, or systems. The technology for the fabrication of micro and nanoelectromechanical systems (MEMS and NEMS) is evolving rapidly, with new kind of device concepts for applications like sensing or harvesting being proposed and demonstrated. However, the characterization tools and methods for these new devices are still not fully developed. Here, we present an on-wafer, highly precise, and rapid characterization method to measure the mechanical, electrical, and electromechanical properties of piezoresistive cantilevers. The setup is based on a combination of probe-card and atomic force microscopy technology, it allows accessing many devices across a wafer and it can be applied to a broad range of MEMS and NEMS. Using this setup we have characterized the performance of multiple submicron thick piezoresistive cantilever force sensors. For the best design we have obtained a force sensitivity $\gamma_F = 158\mu V/nN$, a noise of $5.8 \mu V$ (1 Hz–1 kHz) and a minimum detectable force of $37 \mu N$ with a relative standard deviation of $\sigma_r \approx 8\%$. This small value of $\sigma_r$, together with a high fabrication yield >95%, validates our fabrication technology. These devices are intended to be used as bio-molecular detectors for the measurement of intermolecular forces between ligand and receptor molecule pairs. © 2012 American Institute of Physics.

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I. INTRODUCTION

Microfabricated cantilevers have been used to study numerous physical, chemical, and biological phenomena.¹ Efficient, low-noise transduction of the cantilever motion is critical to the best sensor performance. To that end, optical detection has been commonly used over the past three decades; typically the setup is based on the reflection of a laser beam focused on the cantilever surface. Although optical methods present high displacement resolution and are extensively used, they lose efficiency when downscaling the cantilever dimensions. This limits the applicability of optical detection for cantilevers with high force sensitivity and resolution.² One way to overcome this problem consists in implementing an electrical read-out of the cantilever motion via, for example, piezoresistive effect.³ Silicon cantilevers with piezoresistive transduction were first fabricated at Stanford University⁴ for atomic force microscopy applications. After this, many groups have developed systems and applications based on cantilevers with integrated piezoresistors for force or displacement sensing. These include the development of atomic force microscopy (AFM) probes,⁵–⁸ probes for magnetometry,⁹ the high force sensitivity achievement with <100 nm thin cantilevers,¹⁰ the characterization of low force electrical contacts¹¹ and the study of the ultimate piezoresistive sensitivity at low temperatures.¹²

We have previously presented the fabrication of cantilevers made of polycrystalline silicon using a commercial CMOS process flow together with integrated read-out circuitry.¹³ However, polycrystalline silicon presents lower piezoresistive coefficients than crystalline silicon, which limits the sensitivity of the devices. Recently, we have fabricated crystalline silicon cantilevers, integrating n-doped piezoresistors¹⁴ and n-MOSFET transistors¹⁵ using arsenic impurities in order to obtain shallower junctions. In order to maximize the devices sensitivity, the cantilevers are oriented along the non-standard (100) direction on the wafer surface.¹⁶ A common denominator of different fabrication technologies is the need of testing large number of devices across several wafers. This need is not specific to MEMS and wafer scale testing has been used over the years to test electronic devices. What is specific to MEMS in general, and to piezoresistive cantilevers in particular, is the need to evaluate their sensitivity. That can be done by performing an accurate deflection of the cantilever while recording the resistance variation (i.e., the voltage variation on a Wheatstone bridge). In order to deflect the cantilever with high accuracy, various approaches have been used, such as a needle mounted on a bimorph piezodisk,¹⁷ a calibrated piezotube,¹⁸ an AFM in contact mode¹² or in tapping mode,¹⁹ etc. A parallel approach, only valid for very low stiff devices, measures the resonant thermomechanically-driven displacement fluctuations.¹⁰,¹² All these methods allowed a precise estimation of the device deflection sensitivity but were carried out on a chip level, therefore making technology validation a tedious and long process. An alternative which allows on-wafer testing is to deflect the cantilever by a needle mounted on a micromanipulator,⁸ but that is less accurate than the previously mentioned solutions. Here, we present a novel set of methods that allows on-wafer characterization with high

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accuracy, good repeatability, and unprecedented reduction in testing time. This set of methods, that can be adapted for a broad range of MEMS devices, is analyzed here for the particular case of piezoresistive cantilevers.

Section II presents the theoretical background to analyze the response of piezoresistive cantilevers. Section III describes the methods and experimental setup used to characterize the fabricated devices.

II. THEORETICAL BACKGROUND

The most important parameters determining the performance of a quasi-static force sensor based on piezoresistive cantilevers are the spring constant \( k \) [N/m], the force sensitivity \( (\Delta V/F) [V/N] \), the transducer noise [V] and the minimum detectable force (MDF) [N]. We use simple models which are adequate for our specific cantilevers. For the more general case other models are available which include issues such as non-uniform doping of the piezoresistors.\(^{20,21}\)

A. Spring constant: cantilever mechanical model

The spring constant of a cantilever is defined as

\[
 k = \frac{F}{\delta},
\]

and for a cantilever beam with constant cross section becomes

\[
 k = \frac{3EI}{L^3},
\]

where \( F \) is the perpendicular force acting at the cantilever end, \( \delta \) its deflection, \( L \) the cantilever length and \( EI \) is the bending stiffness of the beam.

To calculate the spring constant of our multilayer, U-shaped cantilever (Figs. 1 and 2), we model it mechanically as follows. We divide it ideally into two parts: the first is the multilayer beam and the second is a single layer beam clamped to the first one both with constant cross sections. Thus, we obtain the cantilever spring constant,

\[
 k = -\frac{3E_1E_2}{(L_1 - L_2)^3(EI_1 - EI_2) - L_2^3EI_2},
\]

where \( EI_1 \) is the bending stiffness of the multilayer part and \( EI_2 \) is the bending stiffness of the second part, which are

\[
 EI_1 = \frac{1}{3}w_1 \sum_{i=1}^{3} E_i (z_{ti} - z_0)^3 - (z_{di} - z_0)^3,
\]

\[
 EI_2 = \frac{1}{12}w_2E_3 (z_{t3} - z_{d3})^3,
\]

where the neutral axis is

\[
 z_0 = \frac{1}{2} \sum_{i=1}^{3} E_i (z_{ti} - z_{di}),
\]

and \( E_1, E_2, \) and \( E_3 \) are the Young’s moduli of silicon nitride, silicon oxide and crystalline silicon, respectively.

The complete description of the fabrication process can be found elsewhere.\(^{14}\)

B. Sensitivity: electromechanical model

When a resistor is mechanically stressed, the relative variation of the resistance value \( (\Delta R/R_0) \) is the result of dimensional resistor changes (length \( l \), width \( w \), and thickness \( t \)) and change in resistivity \( (\rho) \). In the case of low doped crystalline silicon, the change of the resistivity in certain directions (i.e. \((100)\)) is larger than the dimensional changes by a factor of 50, thus the latter can be ignored.\(^{16}\) In case of a uniaxial stress \( (\sigma) \) and parallel current flow, the relative resistance change is

\[
 \frac{\Delta R}{R_0} = \frac{\Delta l}{l_0} + \frac{\Delta w}{w_0} + \frac{\Delta t}{t_0} + \frac{\Delta \rho}{\rho_0} \approx \frac{\Delta \rho}{\rho_0} = \pi_1 \sigma,
\]

where the \( \pi_1 \) is the longitudinal piezoresistive coefficient for that certain direction. The cantilever under study is modeled as a linear elastic beam with a point load at the tip, as explained in Sec. II A. This leads to a longitudinal stress \( (\sigma_x) \) along the two legs of the cantilever that varies linearly along \( x \) and \( z \) and to a transverse stress along \( y \), that we consider negligible. Considering that the resistor, thus the current, and

FIG. 1. (Color online) Exploded view of the piezoresistive cantilever. From the top there are: PECVD silicon nitride (tSiN = 100 nm), thermal oxide (tOX = 38 nm), implanted crystalline silicon, and crystalline silicon (tSi = 325 nm). L1 = 125 \( \mu \)m is half of the length of the piezoresistance and also the length of the multilayer part. L2 = 250 \( \mu \)m is the total length of the cantilever. The implanted piezoresistance is visible on the silicon. \( z_{t0} \) and \( z_{d0} \) are the top and the bottom \( z \) coordinates of the layers, with \( i = 1 \) for silicon nitride, \( i = 2 \) for silicon oxide, and \( i = 3 \) for silicon.

FIG. 2. (a) scanning electron microscope (SEM) image of a piezoresistive cantilever chip: two cantilevers are visible in the top part and the pads for contacting the chip are in the bottom part. (b) SEM image of two piezoresistive cantilevers. In total there are four piezoresistors per chip in a Wheatstone bridge configuration, two in the cantilevers and two in the substrate.
the stress are parallel and along the (100) direction, we can write

$$\frac{\Delta R}{R_0} = \pi_{11} \langle \sigma_x \rangle,$$  \hspace{1cm} (6a)

where $\pi_{11}$ is the longitudinal piezoresistive coefficient for the crystallographic direction (100) and $\langle \sigma_x \rangle$ is the mean stress in the resistor.

Now, considering that for a 1/4 active Wheatstone bridge the relative variation of the output voltage $(\Delta V/V_0)$ corresponds to 1/4 of $\Delta R/R_0$, the force sensitivity is

$$\gamma_F = \frac{\Delta V}{F} = \frac{1}{4} \frac{\Delta R}{R_0} \frac{V_{bias}}{F} = \frac{1}{16} \frac{E_{100}\pi_{11}(2L_2 - L_1)(t_R + 2z_0)}{E_I} V_{bias},$$  \hspace{1cm} (7)

where $E_{100}$ is the Young modulus of silicon along the (100) direction, $t_R$ is the thickness of the resistor (in this case we have approximated the profile of arsenic with a step profile after implantation and thermal treatment) and $V_{bias}$ is the bias voltage applied to the bridge.

C. Noise and MDF

The MDF of the piezoresistive cantilevers is affected by two types of noise sources: electrical and thermomechanical. The two main electrical noises are thermal (or Johnson) noise and $1/f$ noise. The thermal noise has constant power density ($S_T$) [V$^2$/Hz] for every frequency and is caused by the random collisions of the charge carriers. The $1/f$ noise is predominant at low frequency, the power density ($S_{1f}$) decreases as the inverse of the frequency and it is due to carriers mobility fluctuations.

On the other hand, the thermomechanical noise is due to the mechanical vibration of the cantilever caused by the brownian movement of the particles in and around the beam and in this case, the power density ($S_{TH}$) for low quality factors and low frequencies is independent of the frequency. For piezoresistive microcantilevers, it is usually much smaller than the other two noise sources.

The total power noise density can be estimated by adding the three noise power densities,

$$S_V = S_T + S_{1f} + S_{TH} = 4k_BT R + \frac{\alpha V_{bias}^2}{Nf} + \gamma_F^2 \frac{4k_BT}{\omega_0 Q},$$  \hspace{1cm} (8)

where $k_B$ is the Bolzmann constant, $T$ is the temperature, $R$ is the resistance value, $N$ is the number of carriers in the resistor, $f$ is the frequency, $\alpha$ is the Hooge coefficient, $\omega_0$ is the resonant frequency, and $Q$ is the quality factor.

Integrating the total power density in a certain bandwidth, we can estimate the value of the noise voltage ($\sqrt{<V^2>}$) and finally the minimum force that can be detected by the sensor (MDF),

$$\text{MDF} = \sqrt{\langle V^2 \rangle}.$$

III. CHARACTERIZATION METHODS

Performing fast, accurate, and repeatable mechanical, electrical, and electromechanical characterization of the device properties is necessary in order to prove the reliability of the sensor and to validate the fabrication technology in view of future commercial applications. For these reasons, we decided to develop a setup that allows on-wafer properties characterization, which results in a considerable reduction of the testing time.

A. Mechanical properties

The spring constant of the cantilever is calculated according to Eq. (3). The thicknesses are measured using an optical interferometer (Nanospec AFT 200 from Nanometrics). The widths and lengths are verified using optical microscope and SEM images. The Young’s modulus for silicon along (100) direction is taken as $E_S = 130 \text{ GPa}$ and $E_{SOX} = 75 \text{ GPa}$ for thermal oxide. PECVD silicon nitride has been reported to present a wide range of values for its Young’s modulus, from 60 to 300 GPa depending on the deposition conditions and the layer thickness. Thus, it is necessary to experimentally determine such value for our material. We performed micromechanical measurements with a beam bending based technique on stiffer (specifically designed) test cantilevers (T1 design, see Table I) using a commercial AFM (Bruker Dimension 3100). The use of a profilometer is not possible for our cantilevers, due to their shape and their reduced dimensions of the cantilevers. From these measurements, it is easy to calculate the spring constants of the test cantilevers. Then, it is possible to extract the Young’s modulus of the silicon nitride layer using Eqs. (3)–(5). When the AFM probe is in contact with a cantilever, in the contact point we can write the following equation to balance the forces (Fig. 3):

$$F = k_C \delta_C = k_{Pef} \delta_P,$$  \hspace{1cm} (10)

![FIG. 3. (Color online) Beam bending spring constant measurement: the AFM probe (right) is deflecting the test cantilever (left). The initial cantilever and AFM probe positions are shown in dashed lines.](image-url)
where $k_C$ and $\delta_C$ are the spring constant and deflection of the cantilever while $k_{P\text{eff}}$ and $\delta_P$ are the effective spring constant ($k_{P\text{eff}} = k_p\cos^2\alpha$ and $\alpha = 12^\circ$ is the mounting angle of the probe) and the deflection of the AFM probe. We know also that the total probe displacement ($z_P$) is the sum of the deflection of the probe and the one of the cantilever under test, 

$$z_P = \delta_C + \delta_P,$$

therefore we can write

$$k_C = k_{P\text{eff}} \frac{\delta_P}{\delta_C} = k_{P\text{eff}} \frac{\delta_P}{z_P - \delta_P} = k_{P\text{eff}} \frac{\tan \theta}{1 - \tan \theta}, \quad (12)$$

where $\tan \theta$ is the slope of the deflection curve of the AFM probe while deflecting the cantilever (Fig. 4).

For these measurements we used an AFM probe with a measured spring constant $K_{P\text{eff}} = (0.055 \pm 0.002)$ N/m (calibrated using Sader’s tune method\(^{35}\)) in agreement with the theoretical one (0.05 N/m). We initially performed multiple force curves on a single cantilever to study the repeatability of the method from which we obtained a relative standard deviation of 5%. This was then repeated on 9 cantilevers, to study the variation of the spring constant in different points of the wafer. The mean value of the slope, for the different cantilevers, is $\tan \theta = 0.306$ (with a relative standard deviation of 6%) that indicates a spring constant $k_C = (0.0240 \pm 0.0014)$ N/m and therefore an $E_{\text{eff}} = (70 \pm 7)$ Gpa. This value was confirmed measuring the Young’s modulus of the 100 nm thick silicon nitride layer on silicon oxide and aluminum substrate by the nanoindentantion technique (Nano Indenter XP MTS). With this value of the silicon nitride Young modulus, we calculated the spring constant for the different cantilever designs. Note that the rest of piezoresistive cantilever designs have much lower spring constants and it was therefore not possible to perform the spring constant calibration directly on them with this method. The key dimensions and $k$ values of the cantilevers are depicted in the caption of Fig. 1 and in Table I.

### B. Electrical properties

The electrical characteristics of the piezoresistors are measured using a semiautomatic probe station (Karl Süss PA200) and a semiconductor parameter analyzer HP4155. I–V curves are acquired for every tested resistance by applying a bias voltage ranging between –5 V and 5 V while recording the current. We find a good ohmic contact between aluminum and n-type silicon, very high fabrication yield (95%), and low relative standard deviation of the resistance values for both the resistors implanted in the substrate (3%) and the ones integrated on the cantilever (6%) (Table I). The statistics are made out of 24 chips per each design.

The noise power spectral density is measured by using a low noise amplifier (LNA) (SR560 - Stanford Research) and a dynamic analyzer (SR785 - Stanford Research). The pads are contacted by using the probe card specifically designed for the sensitivity characterization (see next paragraph). The measurements are performed in the same probe station used before, which provides also a good electronic shielding from external noise sources. The wiring system and the supply voltage are adapted to reduce as much as possible the non-intrinsic noise.\(^{35}\) For this purpose, we use 4 rechargeable batteries (Eneloop - Sanyo) that give a bias voltage $V_{\text{CC}} = 5$ V and as short as possible coaxial cables. The differential signal is amplified 1000 times by the LNA and the spectra recorded.
by the dynamic analyzer (Figs. 5 and 6). First we tested the repeatability of the measurement, recording the noise spectrum 9 times for 3 different chips obtaining a maximum relative standard deviation of 2% and then we measured the noise spectra of all the chips across the wafer. In Table II, we report the mean voltage noise value referred to input ($V_{\text{noise}}$) measured between 1 Hz and 1 kHz.

### C. Sensitivity and MDF

In order to determine the device sensitivity and therefore the MDF in a fast way, we developed a specific setup to measure it directly on wafer level. For this purpose, we designed and fabricated a special probe-card (Fig. 7) that allows us to contact the pads of our chips and simultaneously deflect them by other means. In particular, its reduced size permits the use of an AFM (dimension 3100) to deflect our cantilevers, thus benefiting from the high motional precision of such equipment.

In our setup, we use the batteries and the LNA SR 560 for biasing the bridge and amplifying the output signal, respectively. We place the whole wafer on the chuck of the AFM. In Fig. 8, it is possible to see how the AFM probe can contact the to-be-tested cantilevers. A stiff AFM probe ($k \approx 40 \text{ N/m}$) is used to deflect the cantilevers at a certain distance from the clamping edge (namely at 140 ± 3 μm or at the free end). The output differential voltage is amplified and recorded continuously together with the AFM probe displacement (Fig. 9). In order to precisely position the AFM tip on top of the device and work under low interaction forces, the AFM is operated in dynamic mode. In this mode, the tip-sample interaction is controlled by maintaining a fixed amplitude of the oscillation of the AFM cantilever at its resonance frequency. This gives us another advantage and is the ability to detect when the AFM probe contacts the piezoresistive cantilever, which is when the oscillation amplitude drops to zero. Since the stiffness of the AFM probe is much bigger than the one of the cantilever under investigation (4 or 5 orders of magnitude), its static deflection is negligible. After one measurement that typically is performed at 1 Hz, the probe card can be moved to the next chip under investigation by using a manual micromanipulator mounted onto the AFM chuck.

The probe-card presented here is specifically designed to be used in the dimension 3100 AFM, can access all the

### TABLE II. Piezoresistive cantilever: deflection sensitivity ($\Delta V/\delta$) at 140 μm from the clamping edge, gauge factor (G), mean force sensitivity ($\Psi_F$) for a force applied at the tip and relative standard deviation (σ), mean voltage noise value referred to input ($V_{\text{noise}}$) measured between 1 Hz and 1 kHz and minimum detectable force (MDF). Statistics were made on 24 chips per each design.

<table>
<thead>
<tr>
<th>Design</th>
<th>$\Delta V/\delta$ ($\mu V/\mu m$)</th>
<th>G</th>
<th>$\Psi_F$ ($\mu V/nN$)</th>
<th>σ</th>
<th>$V_{\text{noise}}$ 1 Hz-1 kHz ($\mu V$)</th>
<th>MDF ($\mu N$)</th>
</tr>
</thead>
<tbody>
<tr>
<td>B1</td>
<td>241</td>
<td>102</td>
<td>158</td>
<td>8%</td>
<td>5.87</td>
<td>37</td>
</tr>
<tr>
<td>B2</td>
<td>231</td>
<td>102</td>
<td>46</td>
<td>8%</td>
<td>5.94</td>
<td>129</td>
</tr>
<tr>
<td>B3</td>
<td>230</td>
<td>102</td>
<td>53</td>
<td>8%</td>
<td>4.95</td>
<td>93</td>
</tr>
</tbody>
</table>

FIG. 5. Noise and sensitivity measurements electrical scheme. The differential voltage of the Wheatstone bridge is amplified by the low noise voltage amplifier. The noise spectrum is recorded by the signal analyzer while the sensitivity signal is recorded by the AFM electronics.

FIG. 6. Input referred noise power spectral density for the Wheatstone bridge (design B2). Integrating the noise between 1 Hz and 1 kHz, we obtain a noise average value of 5.94 μV. The bridge is 5 V DC biased and the pads are contacted by the probe card. Values of the resistances are reported in Table I.

FIG. 7. (Color online) Lateral view (up) and top view (down) of the probe card design (dimensions in mm). For the probes we fixed the maximum vertical drop to 0.7 mm and the length to 16 mm to avoid any interaction with the AFM head.
devices across the 4” wafer and the probes are correctly aligned onto the chip pads using the optical microscope of the AFM. Anyway it can be used (or adapted to) in many other equipments, as for example the probe station KS PA200 (that we already demonstrated in the Sec. III B). The probes are long and almost horizontal in order to avoid any interaction with the AFM head. In addition, the PCB (printed circuit board) has to be small to fit onto the AFM chuck. The two grooves (visible in the left part of the top view) increase the travel range of the micropositioner. The use of the probe-card together with a manual micromanipulator allows characterizing a considerable number of chips (24 per each design) in a relatively short time (10 chip/h) across the whole wafer. This constitutes a definite improvement with the way these measurements were done up to date, where the chip had to be pulled apart from the wafer, attached on a PCB board and wire bonded, which is a time consuming, risky and not robust processes. In addition, with the new method we reduced also the risk to damage the sensor. An additional way to decrease the measurement time would be to automate in part the measurement setup using a motorized micromanipulator. However, there is a limit to the amount of the automation due to the complex nature of the measurement.

Figure 9 shows a typical deflection experiment. From these measurements, we obtain the displacement sensitivity (ΔV/δ) and we can calculate the gauge factor (G) and the force sensitivity (RF = ΔV/F) for a force F applied in the end of the cantilever (Eq. (7)). This technique allows a precise positioning of the tip along the cantilever thanks to the optical microscope and to the motorized positioning stage with a repeatability of ±3 μm, and a very good control of the deflection, due to the piezoactuator of the AFM. In order to check the repeatability of the measurement method, we performed multiple force curves on different piezoresistive cantilevers and we obtained a maximum relative standard deviation of 5% for the displacement sensitivity. This variability is mainly
due to two sources: the low frequency noise of the piezoresistors and the uncertainty of the positioning system. In addition to the repeatability, we studied also the variation of the sensitivity of the chips across the wafer (Fig. 10) and in Table II, we report the mean value of the sensitivities, noise and MDF. This shows that our measurement system is particularly suited to technology characterization where several device types and several device geometries within a type have to be measured. For the best design we calculate a minimum detectable force of 37 pN. The high sensitivity, MDF, yield, and the relatively small deviation in the characteristics are very promising and show a reliable process technology.

IV. CONCLUSIONS

We have presented high throughput characterization methodologies and setups for the on-wafer measurement of mechanical, electrical, and electromechanical characteristics of MEMS piezoresistive cantilever force sensors. The setup is based on the use of atomic force microscope technology (providing high displacement precision) and on a specific probe card that can be integrated within the AFM chuck to drastically reduce the testing time. The development of a fast and reliable on-wafer complete characterization method of the cantilevers was necessary to validate the process technology in view of possible applications. For the best design, the force sensitivity, noise, and MDF are 158  μV/nN, 5.81  μV (1 Hz—1 kHz), and 37 pN, respectively and the device parameter relative standard deviation is 8%. These characteristics allow the cantilevers to be applied as biomolecular detectors for the measurement of the intermolecular binding forces between ligands and receptors that are in the order of hundreds of piconewton for a single pair.36,37

ACKNOWLEDGMENTS

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