Spiers Memorial Lecture

Molecular mechanics and molecular electronics

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We describe our research into building integrated molecular electronics circuitry for a diverse set of functions, and with a focus on the fundamental scientific issues that surround this project. In particular, we discuss experiments aimed at understanding the function of bistable [2]rotaxane molecular electronic switches by correlating the switching kinetics and ground state thermodynamic properties of those switches in various environments, ranging from the solution phase to a Langmuir monolayer of the switching molecules sandwiched between two electrodes. We discuss various devices, low bit-density memory circuits, and ultra-high density memory circuits that utilize the electrochemical switching characteristics of these molecules in conjunction with novel patterning methods. We also discuss interconnect schemes that are capable of bridging the micrometre to submicrometre length scales of conventional patterning approaches to the near-molecular length scales of the ultra-dense memory circuits. Finally, we discuss some of the challenges associated with fabricated ultra-dense molecular electronic integrated circuits.

I. Introduction

Several years ago we began a research program that was focused around the concept of building a molecular electronic computing machine. Our goals were several-fold, including minimization of energy consumption per bit operation, development of computational and memory architectures that are both defect-tolerant and amenable to chemical assembly, and the development of parallel manufacturing approaches that scale to molecular dimensions. The longest term goal is to construct integrated nanoelectronic circuits with multiple functions. These functions include the traditional electronic tasks of routing, logic and memory, but can also include non-traditional tasks such as biomolecular or chemical sensing, photonics, actuation, etc., that are enabled through molecular scale fabrication. Our ‘vision’ for such an integrated circuit is shown in Fig. 1. The crossbar architecture that is the dominant circuit theme shown in the figure is loosely based upon the Teramac supercomputer—a machine that we demonstrated could provide an excellent model system for nanoscale and molecular electronics.

The challenges towards constructing such a multifunctional computational machine are non-trivial. One challenge surrounds the development of patterning techniques that can approach molecular dimensions in terms of both feature size and feature pitch. A second requires the development of architectures that allow for the bridging of length scales between the micrometre and sub-micrometre world of lithographic patterning to the nanometre scale world of self-assembly (the multiplexers of Fig. 1). A third challenge revolves around the development of molecular

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electronic switches. However, the fundamental scientific issues that must be addressed to meet each of these challenges are very rich. In this paper, we will discuss some of our work in these various areas, with a focus on both the fundamental scientific problems that have been addressed, and the outstanding questions that must still be answered.

Note that although many applications are labeled in the Fig. 1 circuit diagram, there is only one architecture—that of the crossbar—for all functions. The major hardware components that differentiate these various functions are the nanowire materials and the molecules that are coated on top of the nanowires or sandwiched in between the junctions. For the memory applications, the molecular systems that we have explored have been bistable molecular mechanical systems known as electrochemically switchable donor–acceptor [2]rotaxanes and [2]catenanes. It is towards these molecules, and the associated molecular electronic devices, that we first turn our attention.

II. Molecular mechanical switches

Over the past several years two previously distinct areas of chemical research—the synthesis of molecular mechanical systems, and the fabrication of solid-state molecular electronics devices and circuits—have been conjoined. The resulting union has led to the scientific demonstration of a number of interesting devices, including molecular electronic switches, light harvesting devices, molecular electronic based random access memory and configurable logic circuits, molecular mechanical biosensors, actuated molecular valves, ion channel mimics, molecular muscles, and novel electrochromic devices. Equally interesting has been the emerging picture of large-amplitude molecular actuation and how it is influenced and modified by the surrounding physical and chemical environments.

In Fig. 2 we present the molecular structure of a bistable [2]rotaxane (RATTF) molecular switch, along with a potential energy diagram describing the bistable nature of the switch. This bistable [2]rotaxane is composed of \( \pi \)-electron-accepting cyclobis(paraquat-p-phenylene)(CBPQT) ring (blue) that encircles either a tetrathiafulvalene (TTF) unit (green) or a 1,5-dioxynaphthalene (DNP) unit (red), both \( \pi \)-electron-donating systems. These mechanically-interlocked molecular compounds and other closely related bistable rotaxanes as well as rotaxanes constructed from different donor–acceptor units and from hydrogen-bonded systems and transition metal templates, have been investigated in depth previously. Under ambient conditions in acetonitrile solution, the CBPQT ring in RATTF encircles the TTF unit preferentially.
(> 90%) with respect to the DNP unit. This equilibrium is described by the $\Delta G^{298\circ}$ change shown in Fig. 2b where $\Delta G^{\circ} = +1.6$ kcal mol$^{-1}$ when the CBPQT$^{4+}$ ring moves from the TTF to the DNP unit. Hence, the co-conformation (CC) with the CBPQT$^{4+}$ ring encircling the TTF is referred to as the ground state co-conformation (GSCC). The first two oxidation states of RATTF$^{4+}$ correspond to the TTF$^0$ $\rightarrow$ TTF$^{1+}$ $\rightarrow$ TTF$^{2+}$ processes. Upon formation of TTF$^{1+}$ cation radical, the Coulombic repulsion between the CBPQT$^{4+}$ ring and the TTF$^{1+}$ results in translation of the ring to the DNP unit. This process is fast (on the millisecond time scale) and is believed to convert all of the GSCC into the MSCC. When the TTF$^{1+}$ cation radical is reduced back to TTF$^0$, the CBPQT$^{4+}$ ring remains around the DNP unit for a period of time. This translational isomer of the GSCC is the metastable state co-conformation (MSCC). Recovery of the MSCC/GSCC equilibrium distribution ($\sim 1 : 9$) is an activated process. This switching cycle can be detected by a number of experimental observations. First, the lowest oxidation potential (corresponding to TTF$^0$ $\rightarrow$ TTF$^{1+}$) of the GSCC is +490 mV, while that for the MSCC is +310 mV. (All potentials referenced to an Ag/AgCl electrode.) Second, the colors of GSCC- and MSCC-dominated solutions are green and red, respectively. Thus, electrochemistry and spectroscopy can be employed to quantify the MSCC/GSCC ratio in such a bistable rotaxane at any given time. Third, the (activated) relaxation of an MSCC- back to a GSCC-dominated distribution is temperature-dependent and so the kinetic parameters may be quantified through time- and temperature-dependent measurements. For example, the $\Delta G^{298\circ}$ for this process in the case of RATTF$^{4+}$ is 16.2 (±0.3) kcal mol$^{-1}$.

Consider, for example, a molecule that, when incorporated into an electronic device, acts as a solid-state molecular resistor$^{17,18}$ or rectifier,$^{19-23}$ or perhaps generates some unusual device characteristic, such as negative differential resistance.$^{24,25}$ Such properties are uniquely studied in the device setting and may arise from a distinctly molecular property, or from the result of the molecule/electrode interaction. When these molecules are studied in the liquid solution phase, those same properties are no longer directly revealed in experiments, although donor–acceptor charge transfer spectroscopy can provide an indirect probe of molecular rectification. In other words, certain properties arise through a combination of the molecular design and the physical (device) environment in which that molecule is incorporated. Molecular mechanical motion, however, has certain key characteristics that are experimentally observable and that are independent of environment. This is especially so for the case of the synthetically flexible catenanes and rotaxanes highlighted in this Discussion.$^{26}$ There are not only multiple ways to design and control molecular mechanical motion in these systems, but there are also a variety of experimental methods that can be employed to interrogate it, regardless of environment.

Our approach towards understanding switching within molecular electronic device settings has been to quantify how the environment of the molecular switch impacts the kinetic and thermodynamic parameters associated with electrochemically driven, bistable molecular mechanical switching. We have established two criteria that define a good molecular switch. First, the thermodynamics and kinetics of the molecular mechanical switching processes should correlate
to those same properties when measured in the solution phase. Second, control molecules shouldn’t switch. The change between the solution phase and the sterically restricted environment of a molecular electronic device is likely to significantly modify at least some subset of the thermodynamic and kinetic parameters for switching. Thus, our approach has been to investigate bistable switching in [2]rotaxanes and [2]catenanes in a host of environments, including the solution phase, solid-state polymer electrolyte gels, self-assembled molecular monolayers tethered to the surface of working electrodes within electrochemical cells and molecular switch tunnel junction devices. Representative images of each of these environments are shown in Fig. 3.

Much of our work on quantitating the switching kinetics and the equilibrium thermodynamics in these various environments has been published elsewhere. From those measurements, the mechanistic picture illustrated in Fig. 4 has emerged as being universal for both [2]catenanes and [2]rotaxanes across all physical environments. We initially discuss the switching kinetics (e.g. measurements of $\Delta G^\circ$ and other associated parameters from Fig. 2), starting with a self-assembled monolayer of bistable [2]rotaxanes on the surface of a working Au electrode. The objectives of these measurements are to quantify the recovery of the ground state equilibrium distribution (described by $\Delta G^\circ$ of Fig. 2 with an equilibrium constant of $K_{D/T}^{4+}$ as shown in Fig. 4) following electrochemical switching of a GSCC-dominated population (ground state) to an MSCC-dominated population (metastable state) of [2]rotaxanes.

Cyclic voltammetry (CV) scans, and accompanying descriptions of the redox processes, are shown in Fig. 5 for the surface-tethered [2]rotaxane and its dumbbell component.

In Fig. 6 we present two CV scans, taken in rapid succession, of just the reversible oxidization of the TTF groups on the same surface-tethered bistable [2]rotaxane.
Note that the first oxidation peak is observed to shift by \( \Delta V \approx -200 \text{ mV} \) between the first and second scans. This hysteresis implies that the CBQT\(^{4+} \) ring encircles the TTF-site during the first scan, but it has not yet returned to the TTF site when the second cv scan was carried out—i.e. it is still on the DN site. If a long time period existed between the first and second scans, then the two scans are observed to overlay (no hysteresis is observed in the first oxidation peak). This is because the ground state equilibrium distribution would have been re-established. Thus, this type of CV data can be measured as a function of temperature and time, and the kinetic parameters associated with the recovery of the GSCC-dominated equilibrium from a MSCC-dominated monolayer can be established. In a similar way, solution-phase data and data within polymer electrolytes can be collected. For all of these environments, the relative GSCC and MSCC confirmations can be established quantitatively. For molecular switch tunnel junction devices, the corresponding experiment is to electronically switch the device from the low- to the high-conducting state, and then follow the time-dependent decay back to the low-conducting state.

Such measurements can be carried out as a function of temperature. In this way, Eyring plots can be constructed for similar molecules in all four environments, and such plots and a table of the derived kinetic parameters are presented in Fig. 7. These type of measurements have been done for several \([2]\)rotaxanes and \([2]\)catenanes, and the picture that has emerged is that the switching (relaxation) kinetics are strongly dependent upon both the molecular structure of the switch, and the physical environment that the switch has been placed in. In fact, both effects can be harnessed to yield approximately equal kinetics control. For example, relaxation times in polymer-based electrochromic devices can be controlled over a dynamic range of approximately \(10^9\) through control over the molecular structure of the switch.\(^5\) Similarly, taking an identical switch (with adaptations to the stopper groups that allow for it to be incorporated into different environments), the relaxation times are reduced by approximately \(10^4\) or \(10^5\) when moving from the solution phase to the polymer to the self-assembled monolayer to the molecular switch tunnel junction device.
We have also measured the equilibrium thermodynamics within a wide variety of environments, and that work will appear in press soon. Briefly, we designed three molecules based upon differential scanning calorimetry measurements of the various host–guest complexes. One molecule (RTTF$_4^1$) utilized the same [2]rotaxane switching motif shown in Fig. 2, but the hydrophilic stopper was replaced with a much bulkier group that contained three polyethylene glycol oligomers. For the second [2]rotaxane (RBPTTF$_4^1$), we replaced the TTF with a bispyrrolotetrathiafulvalene (BPTTF) unit. A third control [2]rotaxane (RBLOCK$_4^1$) was designed with an ethyl group at the base of the TTF group, and this served to lock the CBQT$_4^1$ ring so that it could not translate away from the TTF site, whether or not the TTF site was oxidized. RTTF$_4^1$ was designed to exhibit a temperature-independent ground state equilibrium that is dominated (90%) by the CBQT$_4^1$ ring encircling the TTF site. RBPTTF$_4^1$ exhibits a strong temperature dependent equilibrium that is

![Fig. 6](image6.png)

**Fig. 6** Two cv scans taken in quick succession, following the TTF-oxidation process of the tethered bistable [2]rotaxane on the Au working electrode surface. The TTF$^0$ → TTF$^{2+}$ process exhibits a hysteretic response between the first scan (dashed line) and the second scan (solid line). By integrating these two peaks (green and red) the relative concentrations of the MSCC and GSCC may be evaluated as a function of time and temperature, thereby allowing for a quantitative analysis of $\Delta G^\ddagger$ and associated kinetic parameters.

![Fig. 7](image7.png)

**Fig. 7** Eyring plots for bistable [2]rotaxane molecular switches entrained in four different environments as labeled on the graph. Kinetic parameters extracted from this and similar data are presented in the table. The [2]rotaxane contained TTF and DN recognition units and a CBQT$^{4+}$ ring, but each molecule differed slightly in the terminal stoppers on the dumbbell component. The stoppers were modified to allow for adapting the molecule into the different physical environments.
dominated by very different entropies when the CBQT$^{4+}$ ring encircles the RBPTTF site ($-3.6$ cal K$^{-1}$ mol$^{-1}$) or the DN site ($-30.8$ cal K$^{-1}$ mol$^{-1}$). The entropy associated with the CBQT$^{4+}$ ring encircling the TTF site was ($-22.1$ cal K$^{-1}$ mol$^{-1}$).

The high entropy values are ascribed to configurational entropy that arises from interactions between the CBQT$^{4+}$ ring and the ethylene glycol bridging units. For these measurements, we quantified the amount of GSCC versus MSCC for the solution and polymeric environments using cyclic voltammetry. For the molecular switch tunnel junction environment, we evaluated the temperature dependent switching amplitude. Since the high conductance state is associated with the MSCC, a large ground state equilibrium population of the MSCC will reduce the switching amplitude.

RBLOCK$^{4+}$ was observed to be a poor switch in all environments, as expected. Data for RBPTTF$^{4+}$ and RATTF$^{4+}$ are presented together in Fig. 8. This data provides strong evidence that the ground state equilibrium thermodynamics of these molecules is dominated by molecular structure, with the physical environment of the molecular switch playing only a small role.

**III. Molecular electronic circuits**

One of our motivations for developing molecular switches such as the RTTF$^{4+}$ molecule described above was to be able to apply them as the active (switching) devices within molecular electronic memory circuits. These switches hold a number of advantages (and disadvantages) over more traditional two-terminal switches, such as ferroelectric materials. First, since the switching property is distinctly molecular, devices based upon these switches should scale to near molecular dimensions without a significant change in the switching characteristics. This is distinct from solid state materials such as ferroelectrics. In those materials, a crystallographic domain is ‘poled’ using an electric field applied between the two junctions. For very small junction sizes, and thus small domains, the hysteretic response of the material vanishes. For magnetic materials, this is referred to as superparamagnetism. The second advantage is that the voltage at which the switches are opened or closed is quite stable, since the switching mechanism is electrochemical in nature (e.g. current has to flow in and out of molecular orbitals). For field poled devices, the actual poling process is driven by nucleation, and so is intrinsically statistical. This means that the voltage at which a ferroelectric bit switches can vary from junction to junction, or within a single junction, it varies from cycle to cycle. The net result is that, within a crosspoint memory architecture, it is difficult to get reliable performance from solid-state materials. Our molecular electronic switches, however, work well within a crosspoint environment. A more detailed discussion of this important issue can be found elsewhere.\(^5\)
These molecular mechanical switches are not without their disadvantages. The full switching mechanism contains thermally activated processes, which means that the devices will exhibit temperature-dependent variations in their operation.\textsuperscript{9b} Second, molecular switch tunnel junction devices (MSTJs) made from \textsuperscript{2}rotaxanes and \textsuperscript{2}catenanes have, to date, not been particularly robust performers. For example, a switch that may be cycled 1000 times is, at present, a pretty good switch. Finally, the switching mechanism relies upon high-amplitude molecular mechanical motion, and so it is fairly slow. For different bistable \textsuperscript{2}rotaxanes, Willner’s group has quantified some of the solid-state kinetic processes and has found that the step that corresponds to $kT^* \rightarrow D$ occurs on the millisecond time scale.\textsuperscript{28} Such slow switching speeds, however, are not a particularly serious limitation when compared to the other two issues, since parallel architectures (of which the crossbar is one) generate speed through circuit design, rather than through device switching speed.

A few years ago we reported on a 64-bit molecular electronic memory circuit (Fig. 10).\textsuperscript{5} That circuit utilized bistable \textsuperscript{2}rotaxanes, formed as a Langmuir–Blodgett (LB) monolayer. The LB film was sandwiched between a bottom n-type poly-silicon electrode and a top aluminium electrode. Between the Al electrode and the molecular film was deposited a thin (5 nm) Ti adhesion layer. Using this memory circuit, small data strings could be stored, read out, erased, and then new information could be rewritten into the memory. This memory circuit, considering it was the first of its kind, performed reasonably well.

![Fig. 9](image1)

**Fig. 9** The performance of a 64-bit molecular electronic crosspoint memory circuit (circuit shown in the upper right optical micrograph). The switching elements were bistable \textsuperscript{2}rotaxanes, and the ASCII characters of the text shown above the two traces were stored as 1’s and 0’s, and then read out.

![Fig. 10](image2)

**Fig. 10** A molecular electronic memory circuit in which diodes are constructed at each of the crosspoint junctions by establishing a p to n doping gradient through the junction. The data on the right illustrates the rectifying effect of each junction, while the data on the left illustrates the improved memory performance. Note that absolute current values are utilized to read the status of the memory, and that the switching amplitude is at least a factor of 3 for all bits.
A significant advantage of the crosspoint architecture is that, for a given patterning resolution, it will scale to higher bit densities than any other type of electronically addressable memory, a point which we will utilize below. However, crosspoint architectures also have challenges. Note that for the data of Fig. 9, it is the relative, rather than the absolute current values of the individual bits, that were utilized to determine the state of an individual junction. One of the key issues of a crosspoint architecture is that every junction is electronically coupled to every other junction, and thus changing the status of a few bits actually can affect the absolute current values through all of the other bits. One solution is to construct diodes at each of the junctions. Fig. 10 represents a second generation memory circuit in which we constructed a diode in series with each crosspoint junction by establishing a p–n doping gradient through the underlying poly-Si wire. By thinning the poly-Si wire (using reactive ion etching processes) in the regions between the crossed wires, it was possible to establish current pathways through the junction that passed through a p–n diode prior to passing through the molecular junction. Characteristic data from that memory circuit, are shown in Fig. 10. Note that this memory has a significantly improved performance—the signal level between a ‘1’ and a ‘0’ is recorded as an absolute current value, and the ability to discriminate between a ‘1’ and a ‘0’ is significantly improved. This diode concept also decreases the energy requirements of such a memory, and enables somewhat more flexible operation. In fact, this particular circuit (and the data string shown in Fig. 10) was configured to act as a complementary input XOR wired-logic gate. The details of wired-logic gates may be found elsewhere.

Molecular electronic memory and (wired) logic circuits, based upon bistable molecular mechanical systems, can clearly be constructed using conventional lithographic patterning methods. For example, the data presented in Figs. 9 and 10 was from memories that were patterned using a combination of optical lithography and metals deposition through evaporation shadow masks. However, one of the key advantages to molecular electronics is that it should scale to near molecular dimensions. The challenge of constructing circuits at such dimensions is not unique to the field of molecular electronics. In fact, pushing the limits of patterning methods continues to be a key challenge for much of the electronics industry. The highest resolution patterning method is electron beam lithography (EBL), which can pattern silicon structures with dimensions of just a few nanometers (although with considerable structure-to-structure variability). However, EBL is severely limited in terms of the density of structures that can be patterned. For example, high aspect ratio silicon wires can be patterned at a center-to-center separation distance (pitch) of about 60–70 nm. Thus, the development of methods for fabricating electronic circuits with molecular scale feature sizes and pitches constitutes a significant challenge. We turn now to a discussion of ultra-high density electronic circuitry.

A couple of years ago we reported on the superlattice nanowire pattern transfer, or SNAP, method for constructing ultra-high density nanowire and crosspoint circuits. This patterning method, which translates the atomic-level control over layer thicknesses that can be achieved within GaAs/Al_{x}Ga_{(1–x)}As superlattices into atomic-level control over widths and pitches of nanowires, has been demonstrated to produce nanowire circuits at a density and with a level of fidelity that far exceeds any competing approach. For example, 8 nm wide nanowires patterned at a pitch of 16 nm and with lengths of hundreds of micrometres to a few millimetres have been fabricated. A further advantage of the SNAP method is that it can be utilized to fabricate nanowires of virtually any metal or semiconducting material, including complex structures, such as nanowires constructed of multiple layers of material. We subsequently developed non-destructive doping and reaction ion etching processes for silicon nanowire fabrication. By coupling those processes with the SNAP patterning method, we were able to fabricate silicon nanowires with bulk-like conductivity characteristics. This is significant because of the extreme sensitivity of the electronic properties of one-dimensional conductors towards even very small numbers of defects. Finally, working with colleagues at Hewlett Packard, we have recently reported upon methods for replicating these structures in high throughput. This provides us with the key step towards having a realistic manufacturing method that can approach molecular dimensions in scale.

In Fig. 12 we present scanning electron micrographs of a 4.5 kbit memory. This memory was wired up so that many of the wires were connected to one another to make the testing simpler. Only 64 effective bits, with each effective bit containing between 4 and 200 actual junctions, were individually probed. In the inset of Fig. 12 we present data measured from a similarly fabricated circuit. The data represents the switching responses from several of the smallest of those bits (four
or five junctions each). About half of these devices worked, but the device performance is not too dissimilar to what was observed for the data presented in Fig. 11. Each junction in this circuit is about 10 nm × 40 nm wide, and so contains on the order of 300 molecules. The memory circuits shown in Figs. 9 and 10 each contained on the order of 10^8 molecules or so.

Thus, the circuit of Fig. 12 provides a compelling demonstration that these bits will scale well to reasonably small numbers of molecules. We are currently fabricating and testing 160,000 bit memory circuits fabricated using SNAP (Si) nanowires as the bottom electrodes and SNAP metallic wires as the top electrodes. Those circuits are at a density of 10^11 bits cm^-2, and represent a bit density that would correlate to around the year 2020 according to standard Moore’s law protocols.

Fig. 11  SNAP-patterned silicon nanowires. Each nanowire is 15 nm wide by 30 nm tall, patterned at 34 nm pitch, and approximately 3 mm long. These wires were produced on a Si(111) on insulator (SOI) substrate. The electrical characteristics of a statistical number of these nanowires are shown in the top inset. \( R_0 \) is the calculated resistivity of a nanowire based upon knowledge of the doping level of the SOI wafer. \( R \) is the experimentally measured value. Thus these wires are essentially defect-free as evidenced by the fact that their electrical properties are as good as they can possibly be.

Fig. 12  (Background image) Electron micrograph of a 4.5 kbit molecular electronic memory circuit consisting of 10 nm wide Si bottom electrodes, 40 nm wide Ti/Al top electrodes, and a layer of bistable (RTTF^4^+) rotaxanes sandwiched in between. Each junction contains ~ 300 molecular switches. The inset shows electrical contacts to the Si nanowires, as patterned by electron-beam lithography. Note that each contact touches three to four nanowires. Also shown is the performance of the smallest 13 bits (out of ~24 such bits).
Interconnecting ultra-high density molecular electronics circuits

One final issue that we have addressed involves interconnect architectures that can fully take advantage of ultra-dense electronic circuitry.\textsuperscript{34} Note that in Fig. 12, EBL is not sufficiently high-resolution to enable the addressing of individual nanowires. Architectural concepts for meeting the challenge of electrically addressing (demultiplexing) individual nanowires that are patterned at sub-lithographic densities should satisfy three criteria. First, the demultiplexer architecture must bridge from the micrometre or submicrometre dimensions achievable through lithography to the few-nanometre dimensions achievable through alternative patterning methods.

Second, the architecture should allow for the addressing of many nanowires with a few large wires. Third, the manufacture of the multiplexer should be tolerant of fabrication defects.

Proposed demultiplexer architectures\textsuperscript{35,36} have been based upon combining the concepts of crossbars\textsuperscript{37} (lithographically patterned demultiplexing address wires crossing the nanowires) with multi-input binary tree demultiplexers.\textsuperscript{38} Binary trees, by their very nature, exhibit order $2 \log_2(N)$ scaling, where $N$ is the number of nanowires, and $2 \log_2(N)$ is the number of the large demultiplexing wires utilized to address the nanowires. An example of a binary tree demultiplexer is shown in Fig. 13.

Nanowire assemblies are often characterized by some randomness in organization, as well as defects, such as broken or non-conducting nanowires. To compensate, the proposed demultiplexing schemes have contained a certain amount of redundancy (extra wires). The major cost of this redundancy is that the use of additional address wires implies that certain nanowire addresses will be redundant or non-active, and circuit testing must be carried out to determine the good addresses, and memory must be devoted to storing those addresses. Kuekes and Williams\textsuperscript{35} described a diode- or resistor-based decoder that utilizes $5 \times \log_2(N)$ large microscale address wires crossing an array of $N$ nanowires. DeHon, Lincoln and Savage\textsuperscript{36} described an architecture that utilizes no more than $[2.2 \times \log_2(N)] + 11$ address wires. Their scheme is based upon the field-effect gating of nanowires by the demultiplexer, and requires control over the doping profile along the axial dimension of the nanowires. Such nanowires have been realized experimentally,\textsuperscript{39–41} and Lieber’s group has used them to demonstrate a demultiplexer that bridges fabrication methods (\textit{i.e.} self-assembly versus lithographic patterning), but not length scales.\textsuperscript{42} Both of these architectures are based upon placing controllable regions on the surface of the nanowires. An individual nanowire, which is initially in the non-conducting state, will conduct only when all of the control regions are field- or voltage-addressed—\textit{i.e.} it is the logical equivalent of a multi-input AND gate.

Our approach takes full advantage of the perfect fabrication characteristics of the SNAP patterning method, and it takes full consideration of both the full possibilities and limitations of conventional patterning methods. Fig. 14 illustrates our concept. We have demonstrated this approach, which is described in significant detail elsewhere,\textsuperscript{34} does work for selectively addressing

Fig. 13 A binary tree demultiplexer. The operation of this circuit may be understood by considering each of the eight horizontal wires as having an output that has the truth table of a three-input AND gate, with inputs defined by A, B and C. There are eight possible input addresses, each defining one of the eight horizontal wires. Note that $2^3$ outputs are defined by three input wire pairs, so that the scaling is $2 \log_2(N)$ for $N$ outputs.
Conclusions

In this Discussion we have tried to present some of the key aspects that are required for building a science and technology that is molecular electronics. However we have also just touched at the surface, with only a few of the key issues described, and even those descriptions are not in depth.

One such issue relates to the ability, through thermodynamic and kinetics measurements, to establish a feedback loop between molecular synthesis and the performance of switching molecular electronic devices. Other groups are taking different routes towards establishing similar relationships. However, a second issue, not covered in this paper, relates to achieving control over the molecule–electrode interface, and how to design or choose electrode materials that can take full advantage of molecular properties. Furthermore, the key circuit demonstrations discussed herein are the result of simply copying applications that we already know exist, such as memory and logic. Such existing applications may be the most difficult for a true molecular electronics technology to compete in, but other areas, such as molecular electronics for sensing and responding, mechanical actuation, etc., may be the most promising avenues for real world applications. This is because in certain of those areas, molecular electronics provides a unique and enabling pathway. However, certain of the challenges associated with learning how to manufacture with high precision at the molecular scale are likely to have broader applications than those discussed here. For example, issues of defect tolerant circuitry, ultra-high density patterning, interconnects, etc., are much broader than their applications to molecular electronics. Regardless of what applications eventually emerge, the science that is currently happening is tremendously rich and fun.

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