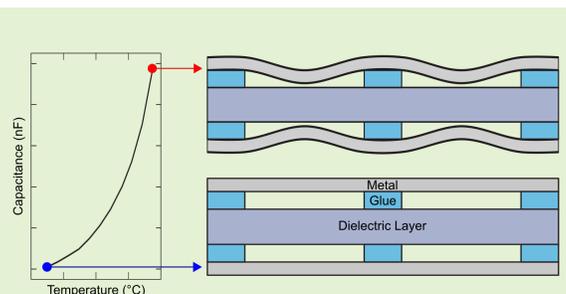


Capacitive temperature sensing via displacement amplification

Semih Taniker, Vincenzo Costanza, Paolo Celli, and Chiara Daraio

Abstract—We propose the realization of capacitive temperature sensors based on the concept of displacement amplification. Our design features two high coefficient of thermal expansion (CTE) metallic layers separated by a low-CTE dielectric layer; conductive and dielectric layers are then separated by a thin air gap and glued together at a few locations. As the temperature increases, the high-CTE layer tends to expand more than the low-CTE one. Owing to the constraint to planar expansion imposed by the low-CTE layer, this results in large out-of-plane displacements of the high-CTE layer – hence the displacement amplification term. In our case, the high-CTE layer buckles and causes a reduction of the gap between conductive and dielectric layers; in turn, this results in a large change of capacitance. First, we illustrate the concept via numerical simulations. Then, we realize a low-cost prototype of such sensor by using aluminum foil as conductor, paper as dielectric, and by gluing the layers together with cyanoacrylate. Our results demonstrate the potential of this simple design as a route towards efficient and low-cost temperature sensors.

Index Terms—Capacitive temperature sensors, Displacement amplification mechanisms, Extreme thermal expansion



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I. INTRODUCTION

Temperature sensors play a fundamental role in countless technologies and industrial applications. The most common type of temperature sensors measure heat transported to the detector through conductive/convective transport. Examples of such sensors are thermocouples, resistance temperature detectors, thermistors and Si- and Ge- semiconductors embedded in integrated circuits [1], [2]. Most of these technologies rely on changes in resistance of some components/materials due to changes in temperature. Few instances of capacitive sensors also exist and display sensitivities that are about 1%/°C lower than those of more established sensing technologies [3]. Unlike contact temperature sensors, IR thermal sensors measure the temperature transported to the detector through infrared radiation. They are attractive because they are non-contact and have the potential to acquire information and detect events even in low-light and poor visual conditions. Among the different IR detectors, bolometers are extremely interesting

for their commercial applications. In a bolometer, the incident IR radiation is absorbed by a black body, which heats up due to the absorbed energy. This change in temperature is then measured by a thermally-responsive layer as a change in resistivity or dielectric constant. Significant efforts have been dedicated to develop and optimize temperature resistive layers for microbolometers, as they represent the principal bottleneck for their sensitivity. Nowadays, the most employed microbolometers are VOx [4]–[6], Si [7] and Si-Ge alloys [8], [9], that display a temperature coefficient of resistance (TCR) between 2%/°C and 5%/°C.

Temperature sensors that rely on electromechanical principles – where the electrical response stems from engineered arrangements of materials that yield desired deformations and changes in resistance/capacitance/dielectric constant – are not common. Examples are capacitive sensors consisting of multi-layer cantilevers that bend in response to differential thermal expansion in each layer [10]–[12]. The bending deformations cause changes in the dielectric constant of one of the inner layers, leading to capacitance variations that can be detected by a readout circuit. However, these technologies are hampered by the fact that the strains achievable in a cantilever are limited, and the changes in dielectric constants with strains are small. Nonetheless, they find applications in MEMS, for example, due to their low energy consumption and low heat dissipation.

In recent years, considerable attention has been devoted to engineering structures with tailored response to environmental stimuli, such as temperature [13]–[18]. Through geometric arrangements of materials with contrasting coefficient of thermal expansion (CTE), it is possible to obtain composite structures

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with extreme values of effective CTE. This includes, for example, structures with zero or negative effective CTE [19]–[21], or structures that can achieve extreme shape changes in response to temperature variations [22]–[25]. In recent work, some of the authors of this letter demonstrated that joining a high-CTE frame with a low-CTE bar allows to produce large tangential displacements of the outer frame [26], owing to displacement-amplification mechanisms [27].

In this letter, we leverage similar displacement amplification mechanisms to create capacitive temperature sensors. We start from thin layers of materials, one conductive and one dielectric, with drastically-different thermal expansion. We show that, by engineering their spatial arrangement as to cause large relative displacements between conductive and dielectric layers, we produce large changes in capacitance per degree, of about 600% over 65°C. Our demonstration is carried out with low-cost sensors fabricated by manually bonding paper and aluminum foil with cyanoacrylate glue. However, especially considering that similar displacement-amplifying mechanisms are common in MEMS [28], our sensors are scalable and could find applications in consumer electronics.

II. UNIT CELL DESIGN

A sketch of a cross-section of the displacement-amplifying structure used to create our sensors is shown in Fig. 1(a). It features two outer metallic layers of high-CTE material (aluminum foil) and an inner low-CTE, dielectric layer (paper). The layers are bonded with cyanoacrylate glue at selected locations, that are chosen to create large air gaps for the metallic layers to expand. As we increase temperature, the high-CTE conductive layers tend to expand (axially) more than the dielectric one. However, this axial expansion is limited by the presence of the low-CTE dielectric and by the fact that the layers are partially glued together [26]. Thus, the conductive layers buckle and bulge tangentially (Fig. 1(c)). The tangential displacement, in particular, is much larger than any axial displacement in the layers – hence the displacement amplification term. This amplified bulging can either be directed inwards or outwards. In our case, the fabrication process biases the outer layers to displace towards the inner one, and to close the air gaps. In turn, this causes an increase in capacitance.

III. THEORETICAL ANALYSIS

Our first step is to perform numerical simulations on the two-dimensional version of our sensor shown in Fig. 1(a). To do so, we resort to the commercial finite element (FE) platform Abaqus/Standard. We select 8-node bi-quadratic plane strain quadrilateral elements to perform the analysis, and we consider geometric nonlinearities. The inner, low-CTE, dielectric layer is modeled as a beam of length $l_d = 20\text{mm}$ and thickness $t_d = 50\mu\text{m}$, and is made of paper, with Young's modulus $E_L = 3\text{GPa}$, Poisson's ratio $\nu_L = 0.25$, coefficient of thermal expansion $\alpha_L = 4 \times 10^{-6}/^\circ\text{C}$. The outer high-CTE layers are here represented by beams of length $l_c = 20\text{mm}$ and thickness $t_c = 12\mu\text{m}$, and are made of Aluminum foil, with $E_H = 70\text{GPa}$, $\nu_H = 0.33$, and $\alpha_H = 23.1 \times 10^{-6}/^\circ\text{C}$ [29]. Our model also includes layers of glue, represented as blocks of

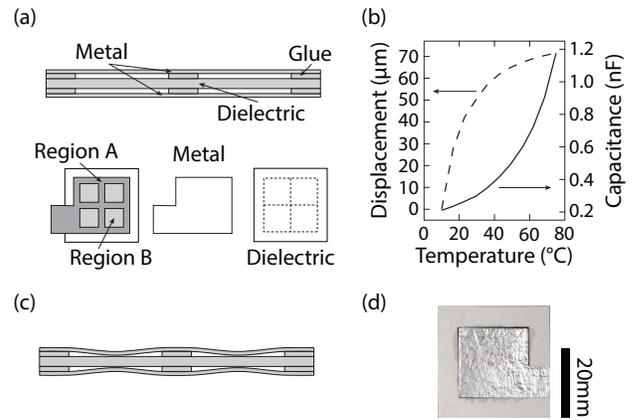


Fig. 1: (a) Schematic representation of the temperature sensing unit, with details of the various layers indicating regions A (glue, dielectric and metallic layers) and B (air, dielectric and metallic layers). (b) Average vertical displacement (dashed curve) and capacitance (continuous curve) vs. temperature of a unit featuring the dimensions written in the main text. (c) Schematic representation of the temperature sensing unit after a temperature increase. (d) A top view of the actual three-dimensional temperature sensing unit.

length $l_g = 2\text{mm}$; since we have little control over the glue thickness, we fit this parameter to match our experimental results. The most accurate fit yields $t_g = 71.56\mu\text{m}$. Finally, we assume the coefficients of thermal expansion to be constant over large temperature ranges.

The simulation results for a single unit are given in Fig. 1(b). We choose $T_i = 10^\circ\text{C}$ as the initial temperature, while the final one is $T_f = 75^\circ\text{C}$. As the temperature rises, the metallic layer expands tangentially; its displacement is shown by the dashed curve. We can appreciate that these displacements are of the order of tens of μm and are large in comparison to the individual layer thicknesses.

Obtaining an accurate measure of the change of capacitance as a function of temperature in our simulations is no simple task, as it requires us to understand the relationship between capacitance and deformation. There are two different cross-sections in our capacitive temperature sensors: one, region A, consists of glue, dielectric and metal layers; the other one, region B, consists of dielectric, metal layers and the air gap. It is important to note that the paper absorbs some of the glue after it is applied, and this causes its dielectric constant to be higher than the nominal value for paper-only. The characterization of the dielectric constant of the glue-impregnated paper is provided in the supplementary material (SM).

In order to obtain the temperature-dependent capacitance of this sensor from our simulations, we need to calculate the capacitance of regions A and B (Fig. 1(a)), and add them up. The capacitance of Region A (made of glue, dielectric and metal layers) is assumed to be constant and is calculated as

$$C_A = \frac{C_g C_{dA}}{C_g + C_{dA}}, \quad (1)$$

where

$$C_g = \frac{\kappa_g \epsilon_0 A_A}{2t_g}, \quad C_{dA} = \frac{\kappa_d \epsilon_0 A_A}{t_d}. \quad (2)$$

The capacitance of the Region B is temperature dependent and it is calculated as

$$C_B(T) = \frac{C_{air}(T)C_{dB}}{C_{air}(T) + C_{dB}}, \quad (3)$$

where

$$C_{air}(T) = \frac{\kappa_{air} \epsilon_0 A_B}{2t_{air}(T)}, \quad C_{dB} = \frac{\kappa_d \epsilon_0 A_B}{t_d} \quad (4)$$

and

$$t_{air}(T) = t_g - \frac{1}{n} \sum_{i=1}^n d_i(T). \quad (5)$$

In Eqs. 1-5, C_A is the capacitance of Region A, C_B is the capacitance of Region B, A_A is the cross-sectional area of region A, A_B is the area of region B, κ_g is the relative permittivity of glue, κ_{air} is the relative permittivity of air, κ_d is the relative permittivity of the dielectric layer (glue-impregnated paper), ϵ_0 is the vacuum permittivity, t_g is the thickness of the glue layer, t_d is the thickness of the dielectric layer, n is the number of nodes corresponding to the lower surface of the metallic layer in the FEM model, and d_i is the displacement of each node. Finally, the total temperature-dependent capacitance of the sensor can be calculated as $C_{total}(T) = C_A + C_B(T)$.

Using the numerical displacement results shown in Fig. 1(b) and this definition of total capacitance, we plot the variation of capacitance as a function of temperature in Fig. 1(b). We can see that the displacement of the conducting layers, as previously claimed, is accompanied by large changes in capacitance, of the order of 1000 pF over a range of 65°C. To validate our numerical predictions, we manufacture and test a set of paper-aluminum sensors, fabricated as described in Appendix I.

IV. EXPERIMENTAL VALIDATION

Capacitors are nowadays fabricated using different approaches (e.g., electrolytic, ceramic, tantalum, etc.). Manufacturers provide various figures of merit to compare the performances of different components. The most important are the frequency behavior, the operating voltage and the temperature dependence. The frequency dependence of a capacitor provides its effectiveness when used in decoupling supplies from the input noise. For this study, measuring the frequency behavior of our sensors is important for two reasons: (1) at the operating frequency, the sensors has to be away from their resonances to avoid unwanted oscillations; and (2) their parasitic resistance and inductance have to be negligible. Therefore, we measure the change of capacitance as a function of frequency in our sample, to select the most effective operating frequency for the readout system (Fig. 5(a) of the Appendix). The drop in the capacitance for increasing frequencies is likely caused by the decrease of the paper dielectric constant, as relaxation processes arise in the paper matrix [30]. In principle, since there is no resonance in the investigated frequency range, we could have chosen any frequency. However, in order to

decrease the effect of parasitics on the measurement, we target the lower frequency spectrum (20 – 50 Hz) to electrically stimulate the sensor. Given the frequency dependence of the capacitance, we measure the dielectric constant through impedance spectroscopy (Fig. 5(b) of the Appendix) in order to fit the experimental results and the numerics. The static permittivity was measured to be 20. However, given the effect of frequency on the capacitance we used the value of 15.81 to fit the experimental data with the numerical simulation. After this study, we finalized the design of the capacitance meter based on the measurement of the phase lag of a first order RC circuit (Fig. 6 of the SM), which was then implemented on a printed circuit board.

The second figure of merit for the capacitive temperature sensor is the operating voltage. The magnitude of the applied field can break the dielectric, or lower the permittivity, by influencing the spontaneous polarization of the capacitor. When measuring the dependence of the capacitance to the applied voltage (100 mVpp, 250 mVpp and 500 mVpp) at different frequencies, we found no differences.

Lastly, we analyze the change of the capacitance as a function of temperature. Standard capacitors are built to have the least change of capacitance with temperature change. In this work, since the paper capacitors are used as temperature sensors, our objective is to obtain a change in capacitance as large as possible for a given ΔT . To assess the performance of the sensors, we cycle the temperature in their environment from 8°C to 45°C for over 5 hours (Fig. 2(a)). The capacitance is acquired by the capacitance meter at a sampling rate of 44S/s. Over this temperature range, the capacitance is measured to vary between ~ 200 pF to ~ 450 pF. As it can be seen in a zoomed-in section of the cycles (Fig. 2(b)) and in the capacitance versus temperature plot (Fig. 2(c)), the relation between temperature and capacitance is nonlinear. In this scenario, a linear temperature coefficient of resistance (TCR) cannot be defined. Therefore, we define the sensor's temperature response as the ratio between the capacitance at the highest temperature (45°C) divided by the capacitance at the lowest temperature (8°C) of the temperature cycle. The response is shown in figure Fig. 2(d) as a function of the cycle number. The capacitance more than doubles over a 37°C interval and is stable over time. The stability over time is also confirmed by Fig. 2(e), where the capacitance at different temperatures (10°C to 45°C in 5°C increments) is plotted versus the cycle number. The capacitance values measured at different temperatures are constant over time.

As shown in Fig. 2(c), due to the thermal inertia of the system, the capacitance versus temperature shows hysteretical behavior, which makes it challenging to effectively compare the experimental and numerical results. Therefore, we characterize the system with temperature steps instead of temperature cycles. By doing so, we allow the temperature on the samples to settle, to account for the thermal inertia of the system and the consequent time, achieving a quasi-static regime. The temperature is controlled via a PID controller and raised from 10°C to 75°C in steps of 5°C, and held constant for 10 minutes (Fig. 3(a)).

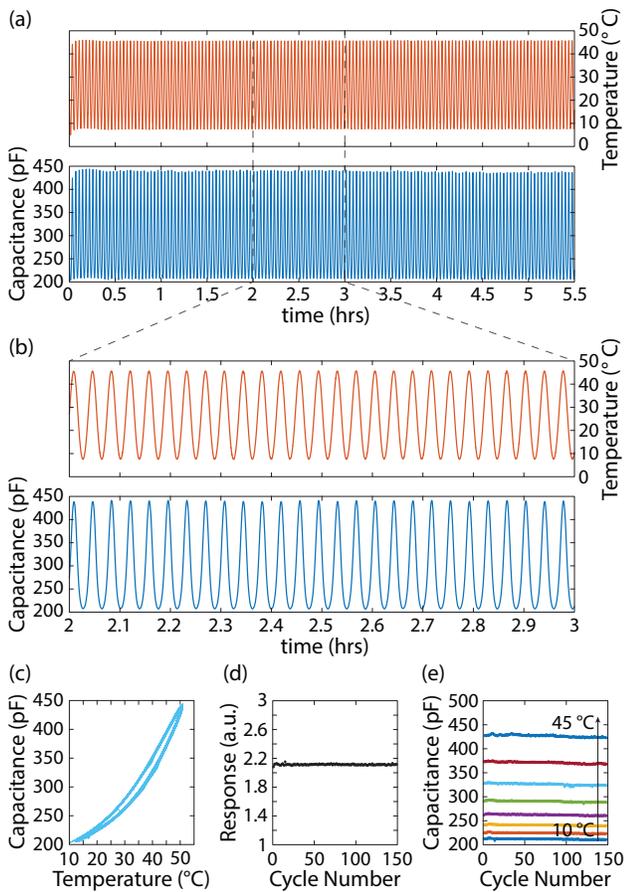


Fig. 2: Cyclic response of the capacitive temperature sensor. (a) Time evolution of the temperature and capacitance over 5.5 hours. (b) Details of the recorded signals, showing some nonlinearity in the capacitance curve. (c) Capacitance evolution as a function of temperature, indicating nonlinearity and hysteric behavior. (d) Evolution of the ratio between capacitance at the highest (45°C) and lowest (8°C) testing temperatures, indicating the stability of our measurements. (e) Capacitance vs. cycle number, for various temperatures, also indicating stability in our measurements.

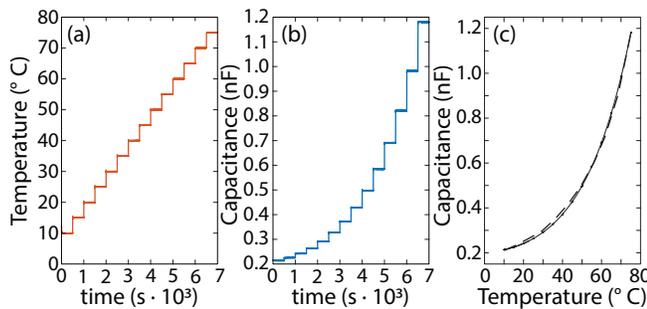


Fig. 3: Comparison of experimental and numerical results. (a) Temperature evolution law used to produce results that are comparable to the numerical curve. (b) Capacitance read-out as a result of the temperature protocol shown in a). (c) Comparison between the numerical capacitance vs. temperature curve and the experimental one, obtained by fitting a line to the curve in (b).

As in Fig. 2, the capacitance closely follows the temperature, going from ~ 200 pF at 10°C to 1200 pF at 75°C, and shows that the sample is stable over time (Fig. 3(b)). The capacitance versus temperature curve measured following this procedure is then compared to the numerical results (Fig. 3(c)), finding excellent agreement.

To test the functionality of the capacitive sensor in a real-world scenario, we use the paper capacitors as IR absorbers. We fabricate a standard paper capacitor following the procedure described in Appendix I, sandwiching a thin piece of paper between two aluminum layers. We apply black paint on top of one of the two aluminum layers to create a surface that absorbs the incident IR radiation. As the radiation is absorbed by the black paint, the temperature of the absorbing layer increases, transferring the heat from the absorbing layer to the aluminum foil; this, in turn, alters the capacitance of the sensor. The sensor is kept on a thermoelectric cooler and a PID controller, in order to keep its temperature constant and to isolate the effects of the absorbed radiation. The radiation source is turned ON and OFF periodically for 10 ms, and the capacitance is measured using the readout systems described in the Appendix III(Fig. 4(a)).

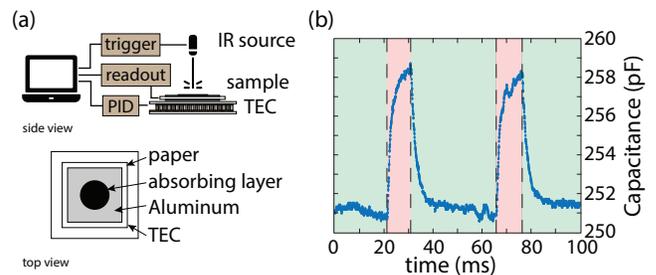


Fig. 4: Response of our capacitive temperature sensor to IR. (a) Experimental setup for testing. (b) Capacitance vs. time. The shaded red and green regions indicate when the IR source is ON or OFF, respectively.

From the time evolution of the measured capacitance (Fig. 4(b)), we can see that this is constant when the IR source is off, while an increase in capacitance is registered when turning on the IR source. we can also see that the sensor recovers after each ON/OFF cycle.

V. CONCLUSION

In this work, we have demonstrated that it is possible to create high-performing capacitive temperature sensors by leveraging mechanical displacement-amplification mechanisms. We have also shown that these sensors can be used for IR sensing applications. The proposed mechanism is suitable for micro-manufacturing and for the creation of low-cost IR sensors. For example, the fabrication of suspended Al membranes on top of a Si base and a SiO₂ dielectric layer is a possible approach [31].

Micro-bolometers cover more than 95% of the uncooled IR detectors market [32]. The most common sensitive layer used in micro-bolometers is vanadium oxide, having a coefficient of resistance at room temperature of more than 4% per degree [33], [34]. Compared to uncooled micro-bolometers, the

proposed micro-mechanism can achieve considerably higher sensitivity. Moreover, spatial arrays of our sensors could be used to create temperature sensing surfaces with finite pixelation, that could find use as low-cost temperature mapping devices in applications where monitoring the temperature is crucial, such as to monitor the health of batteries for electric vehicles.

APPENDIX I MATERIALS AND METHODS

To validate our numerical and theoretical predictions, we manufacture and test a set of bi-material specimens. We use desktop cutting machine (Silhouette Cameo 3) on 50 um-thick sheets of copy paper and 12 um-thick aluminum foil, and we manually glue the Al foils to the paper. The samples were connected to the electronics read-out through copper wires attached via copper tape. The capacitance was measured with three independent systems an LCR meter (BK891, B&K Precision) to measure the capacitance frequency dependence, a lock-in amplifier to measure the static permittivity (MFIA 5 MHz, Zurich Instruments) (see Section II), and a custom-made capacitance - meter system to measure the temperature - capacitance dependence (see Appendix B). The temperature cycling was generated via a peltier element controlled by a specifically designed control PID circuit and measured via a platinum sensor, previously calibrated on a thermal camera (FLIR A655sc).

APPENDIX II CAPACITANCE CHARACTERIZATION

To measure the electrical properties of the fabricated capacitors we measured the capacitance as a function of frequency. We swept the frequency from 20Hz to 10kHz, measuring 1000 different frequencies with the LCR Meter. For each frequency we acquired 50 points and calculated the average of these points. The static permittivity was calculated from the conductivity extracted through an impedance spectrum measured with the lock in amplifier. The real and imaginary part for the conductivity was measured between 100 mHz and 1 MHz at 1V. Then the real and imaginary part of the permittivity was respectively calculated by:

$$\epsilon' = \frac{1}{2\pi f \epsilon_0 \sigma''} \quad (6)$$

$$\epsilon'' = \frac{1}{2\pi f \epsilon_0 \sigma'} \quad (7)$$

where f is the operational frequency, ϵ' is the real part of the permittivity, ϵ'' is the imaginary part of the permittivity, ϵ_0 is the vacuum permittivity and σ is the conductivity of the medium. The static permittivity corresponds to the low frequency plateau of the real permittivity.

APPENDIX III READOUT SYSTEM

The custom-made electronics to measure the capacitance of the sensor is based on the measurement of the phase lag in

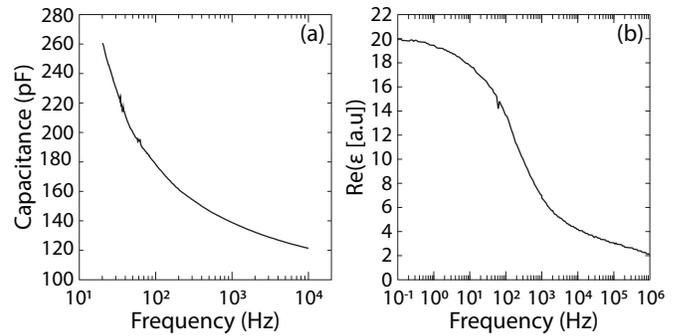


Fig. 5: Frequency dependence of the capacitive temperature sensors. (a) Capacitance as a function of frequency as measured by the LCR Meter. (b) Dielectric permittivity as a function of frequency as measured by the Impedance Analyzer.

a RC circuit. Generally speaking, a first order low pass RC filter shows a drop in the response of -20dB/dec after the cut-off frequency and a phase lag that reaches -45° at the cut-off, while it is 0° one decade before and -90° one decade after. At a constant frequency and resistance, a change in the capacitance will result in a change in the phase lag between the input and the output signals, according to the relation:

$$\Delta\phi = \arctan(2\pi fRC) \quad (8)$$

where f is the operational frequency, R is the resistance and $\Delta\phi$ is the phase lag. By measuring the phase lag, the capacitance can be computed by:

$$C = \frac{1}{2\pi fR} \tan(\Delta\phi) \quad (9)$$

In practice, the phase lag is measured by the time delay between the input sine and the resulting output. The time delay Δt can be converted to a phase lag according to the equation:

$$\Delta\phi = 360^\circ f\Delta t \quad (10)$$

The capacitance can be calculated from the measured time delay as:

$$C = \frac{1}{2\pi fR} \tan(360^\circ f\Delta t) \quad (11)$$

The general structure of the circuit is presented in Fig. 6. The oscillator generates the input sine, the readout section converts the time delay between a reference resistor and the capacitor under test (CUT) into a square wave and the digital counter measures the ON period of the pulse generated by the readout section. The circuit was simulated using LTSpice and then implemented on a PCB manufactured by Eurocircuits. The oscillator to generate the input sine wave was implemented by a buffered quadrature oscillator, where each section provides a shift of -60° so that the three sections combined achieve the -180° necessary for the oscillation. A gain of 8.33 was necessary to sustain the oscillations. The targeted frequency was 20Hz, however, due to the mismatch between the components the circuit oscillates at 44Hz with an amplitude of 500mVpp. At the output of the oscillator a digital potentiometer, directly set by the microcontroller, was added to control the amplitude of the sine (Fig. 6). To avoid the use of a dual supply, which

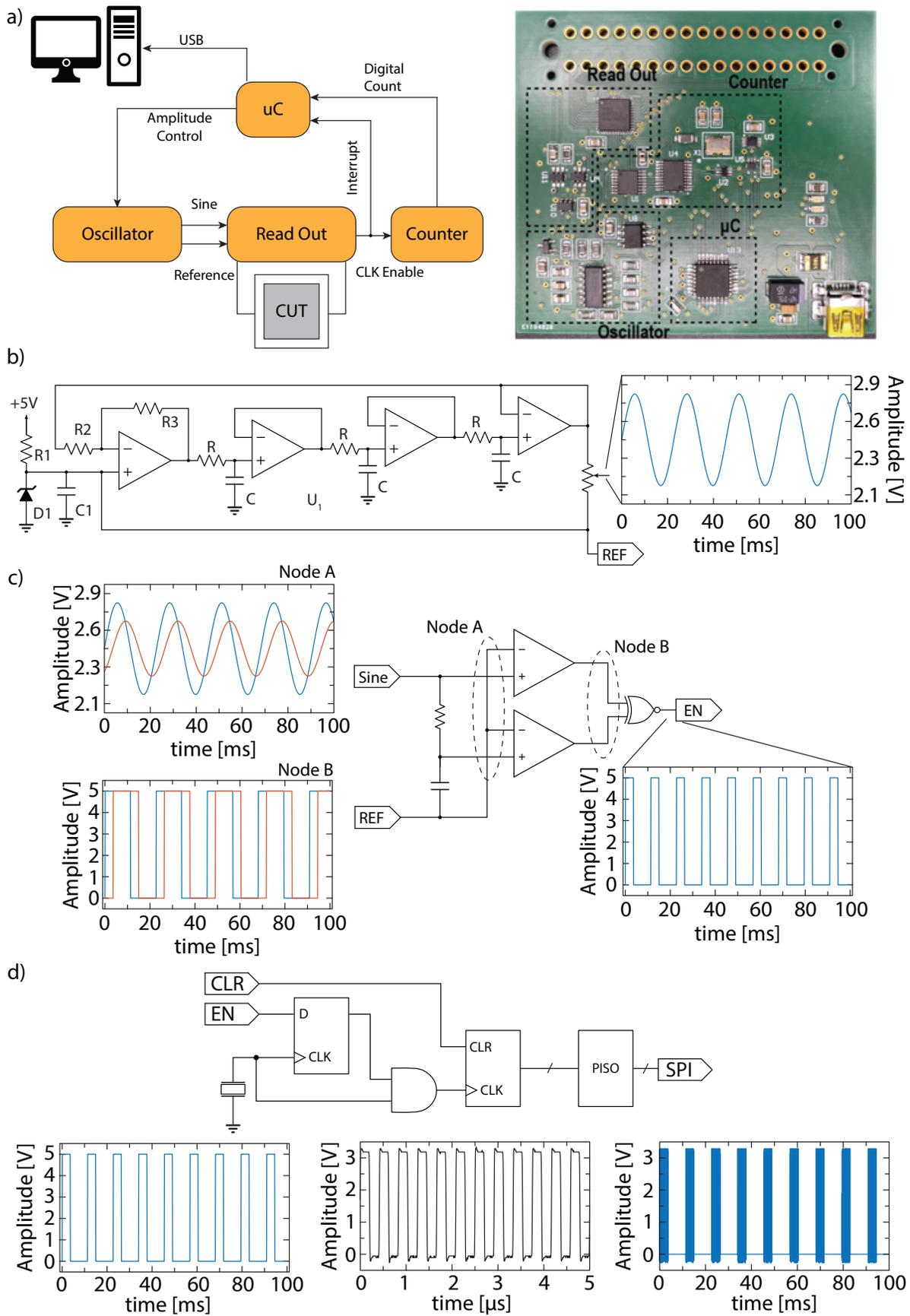


Fig. 6: Capacitive measurement circuit designed to precisely measure temperature-capacitance relation. (a) Logic Diagram. (b) Oscillator. (c) Phase Detector. (d) Phase Counter.

would have required extra components on the board powered via the USB single supply, the circuit was biased at 2.5V through a Zener diode, which was used as voltage reference for the analog part of the circuit. The generated sine wave is sent to the RC section of the circuit, where is converted to a square wave by comparator U2. The delayed voltage across the capacitor is also converted to a square wave through comparator U3. The two resulting square waves are xored to generate a pulse that corresponds to the time delay between the two signals (Fig. 6). U2 and U3 are fast comparators, whose propagation delay (4.5ns) will only add an error of 0.02 fF. The pulse coming from the read-out section is used to gate a 12 MHz clock fed to a 64 bits counter, which will convert the pulse into a digital count. The count is then transferred to the microcontroller via a parallel to serial converter. The time delay is then calculated via the counter count as:

$$\Delta t = \frac{n_{count}}{12MHz} \quad (12)$$

This approach provides a theoretical resolution of 3×10^{-16} F and a dynamic range of 125dB. To validate the circuit, we used different calibration ceramic capacitors (100, 300, 500, 1000 pF, 10%, 50V) that we previously calibrated with an impedance analyzer (MFIA Zurich Instruments, accuracy 0.05%) using the same frequency and output amplitude. The parasitic capacitance was measured by measuring the capacitance without any load (~ 150 pF) and then subtracted to the measured values. The results are summarized in the table below:

Nominal Value (pF)	MFIA (pF)	Capacitance Meter (pF)
100	105	103
300	308	309
500	481	493
1000	977	974

The calibration shows that the capacitances measured by the circuit are within the tolerance of the nominal values of the measured capacitors and good accordance with the capacitance measured with the impedance analyzer.

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