

Tunnel switch diode based on AlSb/GaSb heterojunctions

X.-C. Cheng, X. Cartoixà, M. A. Barton, C. J. Hill, and T. C. McGill^{a)}

Thomas J. Watson, Sr. Laboratory of Applied Physics, California Institute of Technology, Pasadena, California 91125

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We report on tunnel switch diodes based on AlSb barriers and GaSb p - n junctions grown by molecular beam epitaxy. These were the devices with thyristor like switching in the GaSb/AlSb system. The characteristic “S” shaped current–voltage curve was found to occur for structures with AlSb barriers less than 300 Å thick. The switching voltage and current density exhibited less sensitivity to barrier and epilayer thickness than was predicted by the punch-through model. The results were correlated with drift diffusion simulations which have been modified to account for the presence of a tunneling contact. © 2000 American Institute of Physics. [S0021-8979(00)06922-X]

The tunnel switch diode (TSD), also known as the metal insulator semiconductor switch (MISS)¹ or controlled inversion device (CID),² is characterized by its “S” shaped current–voltage (I - V) behavior. The device has unique applications in circuit design due to its large nonlinearity, inherent speed, and integration capability which arises from its vertical structure. For example, a TSD-based, single element static random access memory (SRAM) cell³ has been implemented in the Si system by using SiO₂ as the tunnel barrier on top of a Si p - n junction.⁴ Compared to rival transistor designs, the TSD-SRAM is both more compact due to its structural simplicity and faster because of the tunneling nature of the switching process.

In this article, we demonstrate the successful implementation of the TSD in the GaSb/AlSb system. This device can be seen as the dual complement of the InAs/AlSb/GaSb resonant interband tunneling (RIT) diode⁵ and will add more functionality to antimonide circuits in high speed applications.⁶ The structural dependence of the TSD I - V characteristics is examined, and the results compared to drift diffusion simulation to confirm the effect of tunneling barrier on TSD switching.

The band diagram of the antimonide TSD is shown in Fig. 1. The basic device structure consists of an unintentionally doped AlSb barrier layer in series with a GaSb p - n junction. For the p - n junction polarity shown, the device is forward biased when a positive voltage is applied to the AlSb barrier. Switching from high to low impedance state occurs when the depletion region in the p -GaSb layer punches through to the buried p - n junction, or when the electric field in the depletion region becomes high enough to trigger avalanche multiplication of minority carriers in the layer.⁷ Due to the low doping density of the p -GaSb epilayer, the devices in this study are expected to operate in the punch through mode. The switching voltage is given by

$$V_{\text{switch}} = qN_c(w - w_0)^2 / 2\epsilon_{\text{GaSb}}, \quad (1)$$

where q is the electronic charge, w_0 is the zero bias depletion width of the p - n junction, and N_c , w , and ϵ_{GaSb} are the

doping density, width, and dielectric constant of the p -GaSb epilayer, respectively.¹ The polarity of the buried p - n junction is such that the AlSb layer partially blocks the electron flow in the high impedance state. This is the desired mode of operation because AlSb is a more effective barrier for electrons than holes, i.e., the conduction band offset between AlSb and GaSb is 1.15 eV at the Γ point and 0.55 eV at the X point, which is larger than the valence band offset of 0.4 eV between these materials. The AlSb layer is typically much thicker than the oxide in Si based devices to compensate for its relative lack of barrier height.

As listed in Table I, six different wafers were grown for this study. The AlSb barrier thickness was varied in these samples to delineate its effect on switching, whereas the p -type GaSb layer thickness was varied among 100 Å AlSb barrier samples to study its effect on the switching voltage.

All samples were grown by molecular beam epitaxy on Te doped ($n = 1 \times 10^{18} \text{ cm}^{-3}$) GaSb wafers. The p - n junction was formed at the substrate surface by depositing an unintentionally doped p -type GaSb buffer layer ($p \approx 5 \times 10^{16} \text{ cm}^{-3}$). In order to improve the quality of the p - n junction, the substrate was etched and thoroughly heated for oxide desorption prior to buffer layer growth. At the end of the growth, the structure was capped by a 50 Å GaSb layer to prevent oxidation of the AlSb barrier. The crystal quality was monitored with high resolution x-ray scans, which yielded a full width at half maximum (FWHM) of 18 arcsec for the GaSb epitaxial layer. The wafers were metallized *ex situ* with Au in a sputter deposition tool. Indium left over from the growth served as the back contact. Device mesas were defined by standard photolithography and ranged in size from 38 to 200 μm . Chlorine assisted dry etching was used as the final etch down and yielded highly reproducible surfaces with few etch defects. The I - V characteristics of these devices were measured by using a HP4156 semiconductor parameter analyzer.

The typical I - V characteristic of the antimonide TSD with thyristor-like switching is shown in Fig. 2. As listed in Table I, switching behavior was observed for all devices with 100 and 200 Å AlSb barrier thicknesses. When the AlSb barrier was increased to beyond 300 Å, no switching

^{a)}Electronic mail: tcm@ssdp.caltech.edu

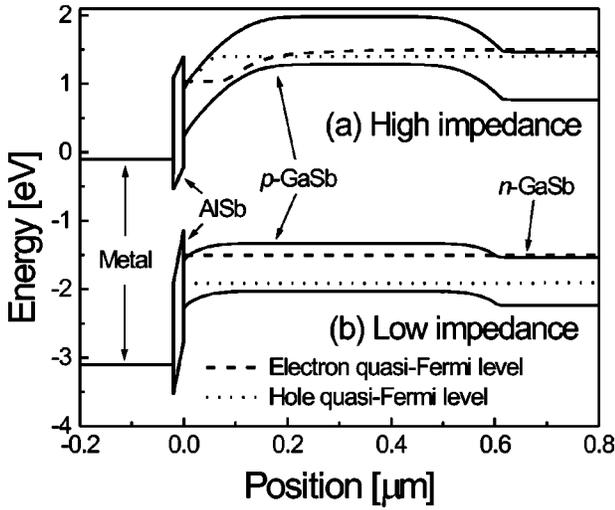


FIG. 1. Band diagrams of an antimonide TSD at a forward bias of 1.6 V. (a) High impedance state with deep depletion in the p -GaSb epilayer. (b) Low impedance state with the p - n junction turned on and most of the bias dropped across the AISb barrier. The energy scale of the high and low impedance states are shifted +1.5 and -1.5 V, respectively, for clarity.

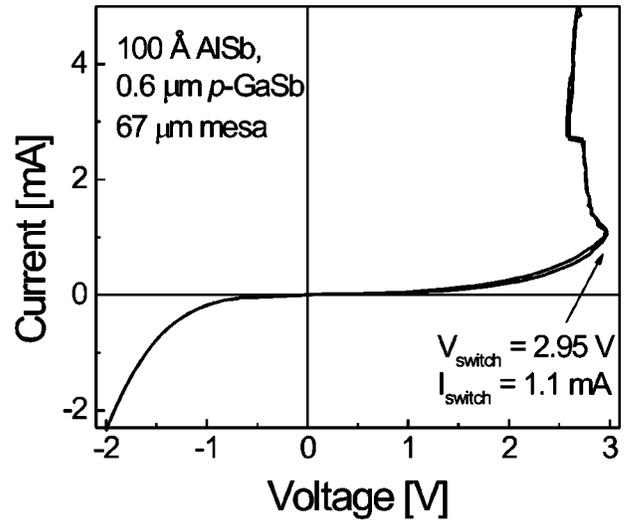


FIG. 2. Current-voltage characteristics of an antimonide TSD with a 100 Å AISb barrier and a 0.6 μm p -GaSb epilayer. The reverse current was large and varied exponentially with voltage, indicating that tunneling through the buried p - n junction was significant. The less than ideal quality of the p - n junction was expected since it was formed at the substrate surface.

was observed. The device instead remained in the high impedance state under forward bias up to a breakdown voltage of 5–7 V. Typical switching current densities ranged from 10 to 300 A/cm², and had a much weaker dependence on the AISb barrier thickness than expected from theory.⁷ Similar insensitivity of switching current to barrier thickness has also been observed in Si/SiO₂ devices.⁸ The switching voltage ranged from 2.3 to 3.5 V and was relatively independent of p -type GaSb epilayer thickness. This result did not agree with the punch through model, which predicts a lower switching voltage for thinner GaSb epilayers.

Similar to Si TSDs,⁸ the low impedance branch of the I - V curve shifted slightly upward when the device was current stressed. The breakdown current density for the TSD device was on the order of 10³ A/cm². Post breakdown characteristic was p - n diode like, indicating that the breakdown occurred in the AISb barrier. For a number of devices, the switching behavior was also modified following significant current stressing. As shown in Table I, the after stress switching voltage and current agglomerated around 1.2 V and 0.2–1 mA. Stress induced modifications have been observed in TSDs in other material systems,⁹ but such agglom-

TABLE I. Antimonide TSD structures fabricated and the observed switching characteristics. Device size was 67 μm.

Device configuration		Switching voltage and current	
AISb barrier	p -GaSb epilayer	Initial values	After stressing
100 Å	0.2 μm	2.3 V/2.0 mA (unstable)	1.2 V/1.0 mA
100 Å	0.4 μm	3.5 V/3.0 mA	1.2 V/1.0 mA
100 Å	0.6 μm	3.0 V/2.1 mA	1.3 V/0.2 mA
200 Å	0.6 μm	3.1 V/1.8 mA	1.2 V/0.3 mA
300 Å	0.6 μm	no switching observed	
500 Å	0.6 μm	no switching observed	

eration of post stress switching voltages and currents was unique to the antimonide device. We speculate that this was due to activation of a deep level in the AISb layer or possible modification of surface charge at the AISb to p -GaSb layer interface.

Since the TSD switching was strongly dependent on the AISb barrier thickness, we have used a computer model¹⁰ to simulate the I - V characteristics of device structures with different AISb barriers. The simulation was based on the Poisson and the carrier continuity equations¹¹

$$\nabla^2 \phi - \frac{q}{\epsilon} (n - p - C) = 0 \quad (2)$$

$$\nabla \cdot \mathbf{J}_n - qR(\phi, n, p) = 0, \quad (3)$$

$$\nabla \cdot \mathbf{J}_p + qR(\phi, n, p) = 0 \quad (4)$$

with

$$\mathbf{J}_n = qD_n \nabla n - q\mu_n n \nabla \phi, \quad (5)$$

$$\mathbf{J}_p = -qD_p \nabla p - q\mu_p p \nabla \phi, \quad (6)$$

where ϕ is the electric potential, n and p are the electron and hole concentrations, q is the electron charge, ϵ is the semiconductor dielectric constant, C is the net concentration of ionized dopants ($N_d^+ - N_a^-$), \mathbf{J}_n and \mathbf{J}_p are the electron and hole current densities, R is the net recombination rate, D_n and D_p are the electron and hole diffusion constants, and μ_n and μ_p are the electron and hole mobilities. The boundary conditions at the surface of the barrier are determined by the generation-recombination component and the tunneling current according to

$$\mathbf{e}_n \cdot \mathbf{J}_n = -qR_s + J_{n\text{calc}}, \quad (7)$$

$$\mathbf{e}_n \cdot \mathbf{J}_p = qR_s + J_{p\text{calc}}, \quad (8)$$

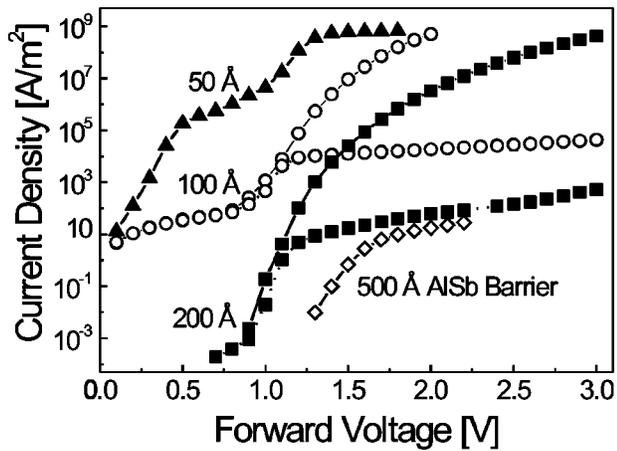


FIG. 3. Simulated characteristics for TSD devices with $0.6 \mu\text{m}$ $p\text{-GaSb}$ epilayer and different AlSb thicknesses. Bistable states were obtained for 100 and 200 Å barrier thicknesses. Only the low impedance state was obtained for the barrier thickness of 50 Å whereas the 500 Å barrier yielded only the high impedance state.

where \mathbf{e}_n is a unit vector perpendicular to the AlSb/GaSb interface, R_s is the generation-recombination rate, and $J_{n\text{calc}}$ and $J_{p\text{calc}}$ are the calculated electron and hole tunneling currents, respectively.

To reduce the computational load, we have employed the Scharfetter–Gummel scheme¹¹ to discretize the equations and the Newton–Raphson method to solve the resulting set of nonlinear algebraic equations. The multivalued nature of the TSD I – V curve contributed much to the complexity of the simulation. In the negative differential resistance region, the device presented two stable current values for a fixed voltage, hence the convergence of the simulation to a particular solution was strongly dependent on the initial condition.

Figure 3 shows the simulated I – V characteristics of TSD devices with AlSb barriers ranging from 50 to 500 Å. It

can be seen that only the low impedance state existed for the 50 Å AlSb barrier device whereas the 500 Å barrier device yielded only the high impedance state. Dual impedance state behavior was obtained for AlSb barrier thicknesses of 100 and 200 Å, which agreed with experimental findings.

In summary, we have fabricated the antimonide tunnel switch diode and observed successful operation of the device. Switching was obtained for AlSb barrier thicknesses between 100 and 300 Å, as was predicted by drift diffusion simulations. The switching voltage and current density had a weak dependence on the barrier and epilayer thickness and did not agree with the punch-through model. Modification of the switching behavior following current stressing was also observed and was attributed to instability associated with the AlSb layer. These deviations from the expected behavior indicate the importance of developing a better AlSb layer and AlSb/GaSb interface.

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